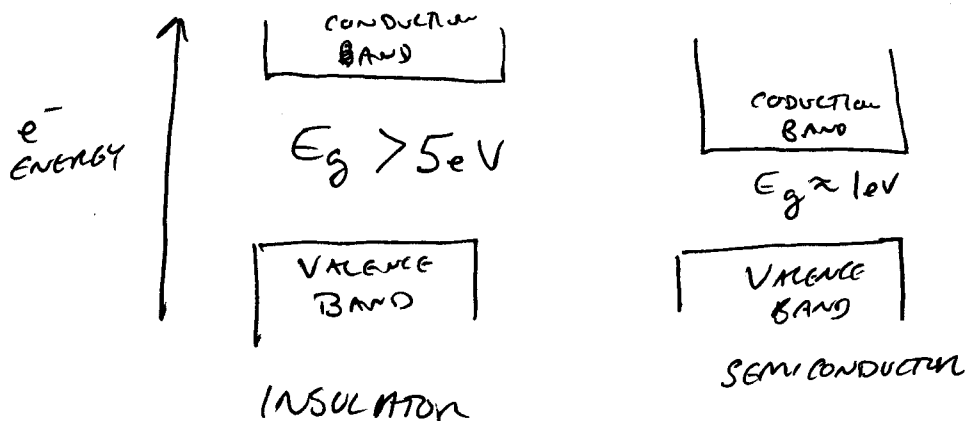


SEMI-CONDUCTOR DIODE DETECTORS

✓

- SMALLER THAN GAS DETECTORS BECAUSE DENSITY $\sim 1000\times$
- COMPARED TO SCINTILLATION DETECTORS (WHICH HAVE RELATIVELY POOR ENERGY RESOLUTION) + MULTIPLE MANY INEFFICIENT STEPS.
 $\sim 100\text{ eV}$ IS REQUIRED TO PRODUCE 1 INFO CARRIER
 $\sim 6\% \rightarrow 10\%$ FWHM
- NEED TO INCREASE # OF INFORMATION CARRIERS PER PULSE.
- SILICON PREDOMINATES IN THE DIODE DETECTORS FOR CHARGED PARTICLE SPECTROSCOPY.
- BAND STRUCTURE;



• CHARGE CARRIERS

AT NON-ZERO TEMPERATURES IT IS POSSIBLE FOR VALENCE ELECTRON TO BE EXCITED ACROSS THE GAP TO THE CONDUCTION BAND.

- AGAIN CREATING AN $e^- - h$ PAIR
- WHICH MAY UNDER THE INFLUENCE OF AN E -FIELD.

PROBABILITY $e^- - h$ PAIR IS THERMALLY GENERATED IS GIVEN BY

$$P(T) = C T^{3/2} e^{-\frac{E_g}{2kT}}$$

$\xrightarrow{\text{ABS TEMP}}$ C $\xrightarrow{\text{BANDGAP ENERGY}}$ E_g

$\xrightarrow{\text{PROPORTIONALITY CONSTANT CHARACTERISTIC OF THE MATERIAL}}$ $T^{3/2}$

	<u>Si</u>	<u>Ge</u>
Z	14	32
ATOMIC WEIGHT	28.09	72.60
ρ - - -	2.33 g/cm ³	5.328/cm ³
$\frac{\text{ATOMS}}{\text{cm}^3}$ - - -	4.96 x 10 ²²	4.41 x 10 ²²
FORBIDDEN ENERGY GAP @ 300K	1.115 eV	0.665 eV
" @ 0K	1.165 eV	0.746 eV

MIGRATION OF CHARGE

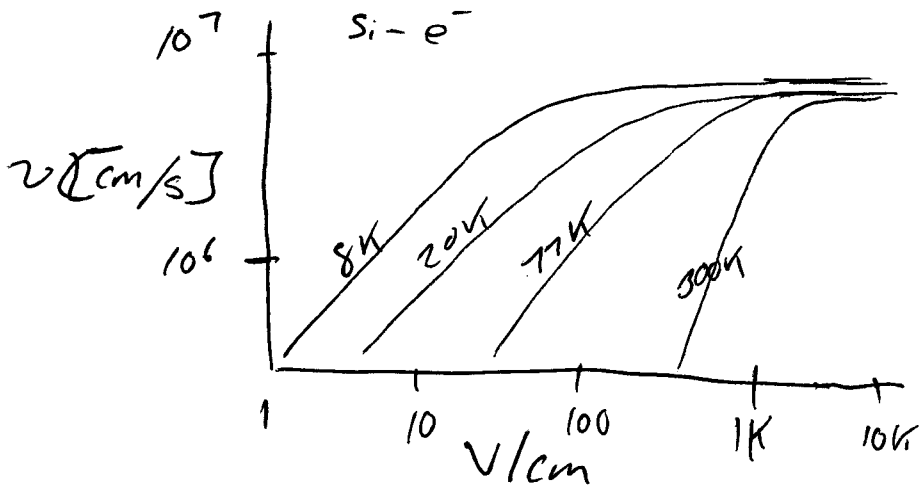
AT "LOW-TO-MODERATE" VALUES OF E-FIELD

DRIFT VELOCITY IS PROPORTIONAL TO APPLIED E

$$v_H = \mu_h E$$

$$v_{e^-} = \mu_{e^-} E$$

AT HIGHER E -FIELD, SATURATION VELOCITY IS REACHED



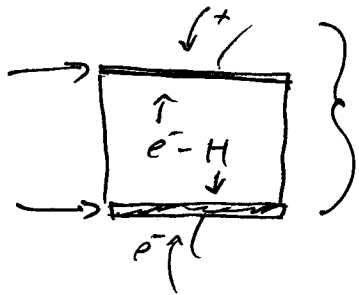
• COLLECTION TIME AT SATURATION VELOCITY
~ 10 ns

SEMI-CONDUCTORS AS RADIATION DEFECTORS

4

- COLLECTION PATH FOR CHARGE CARRIER

→ OHMIC CONTACTS



ELECTRICAL CHARGE EQUILIBRIUM IS MAINTAINED.

- LEAKAGE CURRENT

RESISTIVITY OF THE HIGHEST PURITY SILICON AVAILABLE IS $50,000 \Omega\text{-cm}$.

1-mm THICK SLABS INTO 1cm^2 WITH OHMIC CONTACTS

$$R = 5000 \Omega$$

$$500\text{V} \rightarrow 0.1 \text{ AMP}$$

CONTRASTING 10^{-6} AMP FROM A SUBSTATION RADIATION DOSE.

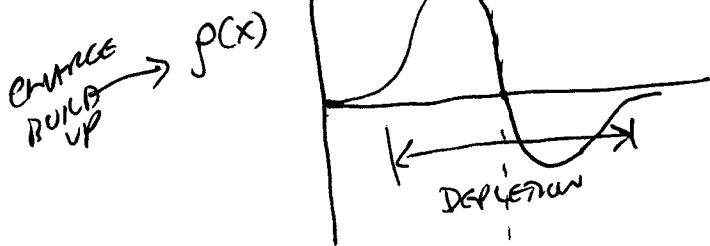
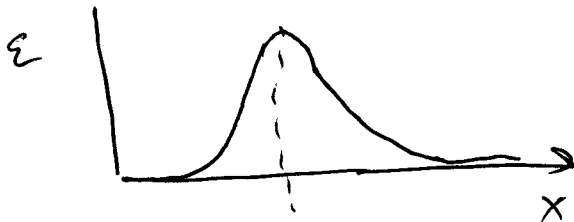
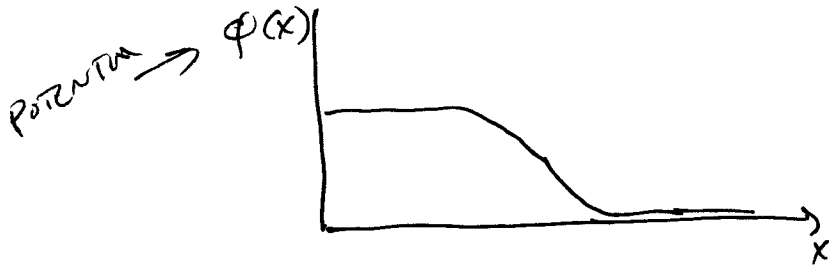
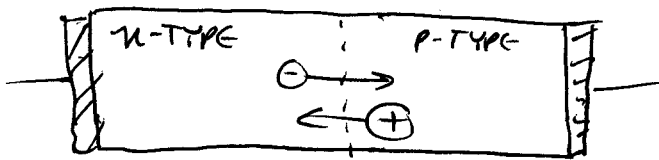
BULK LEAKAGE IS REDUCED BY BLOCKING CONTACTS
LEAKAGE IS TYPICALLY BROUGHT BELOW 10^{-7} AMPS

→ SURFACE LEAKAGE. BECOMES DOMINANT

SEMICONDUCTOR PHYSICS

TO REDUCE BULK LEAKAGE, NON-INJECTING/BLOCKING ELECTRODES SUCH AS TWO SIDES OF P-N JUNCTION. I.E. IT IS DIFFICULT TO INJECT e^- FROM P SIDE; & HOLES ARE NOT EASILY INJECTED

SEMI-CONDUCTOR JUNCTION



$$\nabla^2 \phi = -\frac{\rho}{\epsilon} \leftarrow \text{dielectric const}$$

$\sim 1-D$

$$\frac{d^2 \phi}{dx^2} = -\frac{\rho(x)}{\epsilon}$$

$$\epsilon = \vec{E} = -\text{GRAD } \phi$$

$$\epsilon = -\frac{d\phi}{dx}$$

REVERSE BIAS:

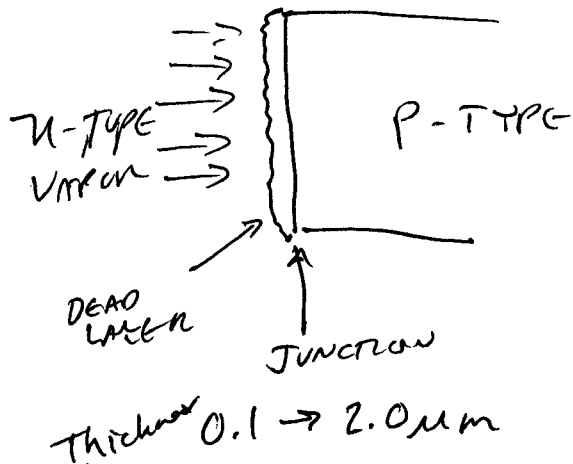
- ALMOST ALL OF THE APPLIED VOLTAGE WILL APPEAR ACROSS THE DEPLETION REGION
- MAX E-FIELD WILL OCCUR AT THE N-P-TYPE TRANSITION

$$E \approx \frac{2V}{d} = \left(\frac{2VN_e}{\epsilon} \right)^{1/2} \approx 10^{+6} / 10^{+7} \text{ V/m}$$

\uparrow
depletion thickness layer

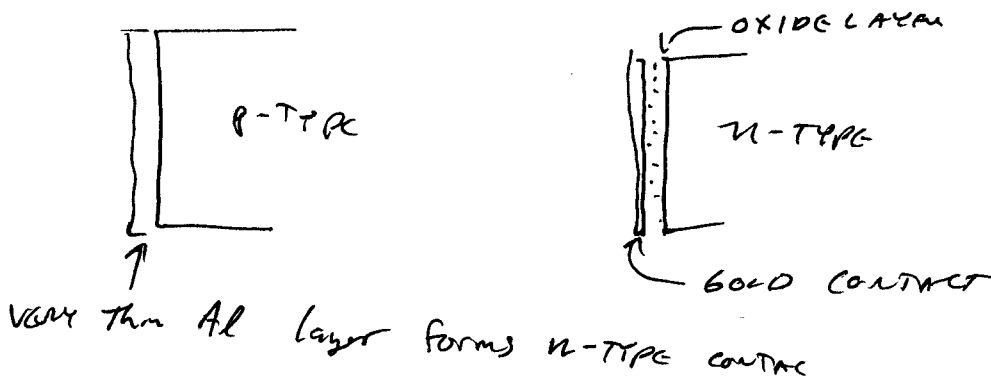
FABRICATION OF P-N JUNCTION DETECTORS

1. DIFFUSED JUNCTION



RADIATION MUST PASS THRU DEAD LAYER, LEADING TO LOST dE/dx INFORMATION

2. SURFACE BARRIER DETECTORS, WHERE JUNCTIONS LIES AT SURFACE



3. ION IMPLANTATION

DOPING IMPURITIES AT SURFACE BY ACCELERATING ION BEAM + IMPLANTING INTO THE SURFACE.

WHICH FORM n^+ FROM PHOSPHORUS
 p^+ FROM BORON

IMPLANTATION ENERGY $\sim 10\text{keV}$ MONOENERGETIC

\hookrightarrow WHAT IS DEPTH?

DETECTOR NOISE & ENERGY RESOLUTION

SOURCES

REDUCTION
TEMP.

FABRICATION
TECHNIQUES

ASSOCIATED THICKNESSES
WITH ~~DEPLETION~~ REGION
(FABRICATION \uparrow
BUS V)

- 1. FLUCTUATION IN LEAKAGE CURRENT, SHOT NOISE
- 2. FLUCTUATION IN SURFACE LEAKAGE CURRENT
- 3. JOHNSON NOISE: ASSOCIATED W/ SERIES RESISTANCE & BAD CONTACTS.

$$\Delta E_{\text{noise}}^2 = \Delta E_{\text{bulk}}^2 + \Delta E_{\text{surf}}^2 + \Delta E_{\text{Johnson}}$$